

**Amendments to the Specification:**

Please replace the paragraph beginning with “In Fig. 4” on page 7, line 3, with the following paragraph:

In Fig. 4, there is shown an implementation of SPDT switch 32 of FIG. 2. Like reference labels in Fig. 4 refer to the same features described in Fig. 2. SPDT switch 32 is implemented using an integrated circuit RF SPDT switch 58. This is preferably a MOSFET switch. For example, a PE4230 available from Peregrine Semiconductor Corp., can be used. This integrated circuit features high RF isolation (38 dB at 1,0 GHz) and low insertion loss (0,44 dB at 1,0 GHz) and makes use of low voltage CMOS logic control.